

Figure 1

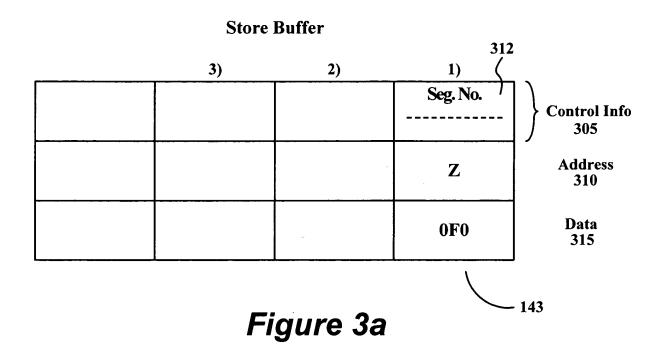
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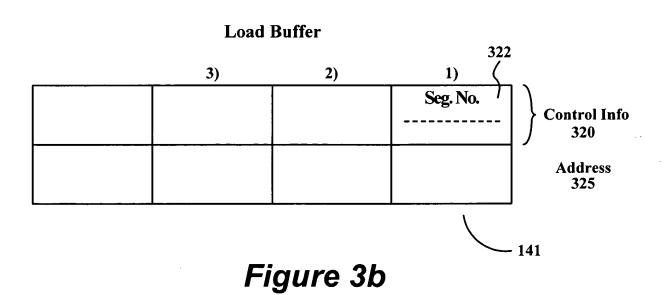
Load Buffer 141

Store Buffer 143

Memory Ordering Buffer (MOB) 127

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Operation		Source	Destination	
1)	Store	R4,	[R1]	
2)	Load	[X],	R2	

Reg.	Correct Value	Incorrect Value
R1	X	Y

Figure 4a

L0 Cache System 120

Address	Data	
X	F000	

Figure 4b

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Store Buffer

 2)	1)	
	Seg. No. 512 Invalid Store Flag 510	Control Info 505
	Y	Address 310
	FOFO	Data 315
	43A	

Figure 5

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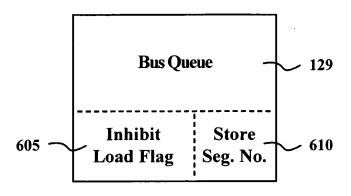


Figure 6

Store Buffer

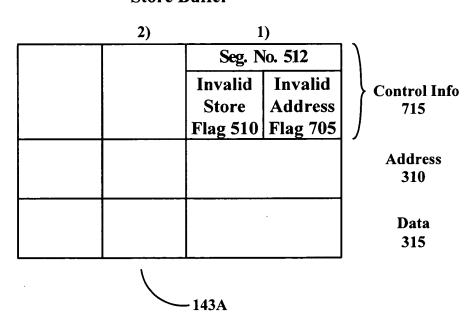


Figure 7